

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor:	Robert M. Zeidman	§	Atty.Dkt.No.:	6257-16302
Serial Number:	10/044,217	§§	Examiner:	Luu, Cuong V.
Filing Date:	November 19, 2001	§§	Group/Art Unit:	2128
Title:	SYSTEM AND METHOD FOR CONNECTING A LOGIC CIRCUIT SIMULATION TO A NETWORK	§§	Conf. No.:	9153

\*\*\*\*\*CERTIFICATE OF E-FILING TRANSMISSION\*\*\*\*\*

I hereby certify that this correspondence is being transmitted via electronic filing to the United States Patent and Trademark Office on the date shown below:

On: December 17, 2010 /Alex A. Courtade/  
Date Alex A. Courtade, #65,635

**RESPONSE TO OFFICE ACTION MAILED SEPTEMBER 17, 2010**

This paper is submitted in response to an Office Action of September 17, 2010, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.